

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: H. TANAKA  
Serial No.: (new continuation application under 37 CFR §1.53[b] of  
application Serial No. 10/179,222)  
Filed: August 21, 2003  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
Group: 2816  
Examiner: T. CUNNINGHAM

**CLAIM FOR FOREIGN PRIORITY**

**MS: PATENT APPLICATIONS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

August 21, 2003

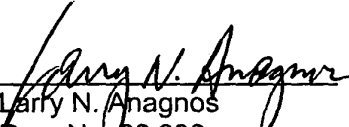
Sir:

Pursuant to 35 U.S.C. §119 and 37 CFR §1.55, applicants hereby claim the  
right of priority based on the following foreign application filed in Japan:

**Japanese Patent Appln. No. 11-135557, filed May 17, 1999.**

A certified copy of this foreign application was filed on May 24, 2000 in prior,  
parent appln. Ser. No. 09/565,116, filed on May 4, 2000.

Respectfully submitted,  
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